

## SECTION G — PHYSICS

## G06 COMPUTING; CALCULATING; COUNTING

**G06F ELECTRIC DIGITAL DATA PROCESSING** (computers in which a part of the computation is effected hydraulically or pneumatically G06D, optically G06E; computer systems based on specific computational models G06N; impedance networks using digital techniques H03H)

**Note(s)**

In this subclass, the following terms or expressions are used with the meaning indicated:

- "handling" includes processing or transporting of data;
- "data processing equipment" means an association of an electric digital data processor classifiable under group G06F 7/00, with one or more arrangements classifiable under groups G06F 1/00-G06F 5/00 and G06F 9/00-G06F 13/00.

**Subclass index**

DATA PROCESSING.....	7/00, 15/00-19/00
INPUT, OUTPUT; INTERCONNECTIONS BETWEEN FUNCTIONAL ELEMENTS.....	3/00, 13/00
ADDRESSING OR ALLOCATION.....	12/00
CONVERSION; PROGRAMME CONTROL; ERROR DETECTION, MONITORING.....	5/00, 9/00, 11/00
DETAILS.....	1/00
SECURITY ARRANGEMENTS.....	21/00

**1/00 Details not covered by groups G06F 3/00-G06F 13/00 and G06F 21/00** (architectures of general purpose stored programme computers G06F 15/76) [1, 2006.01]

- 1/02 • Digital function generators
- 1/025 • • for functions having two-valued amplitude, e.g. Walsh functions [5]
- 1/03 • • working, at least partly, by table look-up (G06F 1/025 takes precedence) [5]

**Note(s)**

In order to be classified in this group, the table must contain function values of the desired or an intermediate function, not merely coefficients.

- 1/035 • • • Reduction of table size [5]
- 1/04 • Generating or distributing clock signals or signals derived directly therefrom
- 1/06 • • Clock generators producing several clock signals [5]
- 1/08 • • Clock generators with changeable or programmable clock frequency [5]
- 1/10 • • Distribution of clock signals [5]
- 1/12 • • Synchronisation of different clock signals [5]
- 1/14 • • Time supervision arrangements, e.g. real time clock [5]
- 1/16 • Constructional details or arrangements (instrument details G12B) [5]
- 1/18 • • Packaging or power distribution [5]
- 1/20 • • Cooling means [5]
- 1/22 • Means for limiting or controlling the pin/gate ratio [5]
- 1/24 • Resetting means (micro-programme loading G06F 9/24; restoration from data faults G06F 11/00) [5]

- 1/26 • Power supply means, e.g. regulation thereof (for memories G11C) [5]
- 1/28 • • Supervision thereof, e.g. detecting power-supply failure by out of limits supervision [5]
- 1/30 • • Means for acting in the event of power-supply failure or interruption, e.g. power-supply fluctuations (for resetting only G06F 1/24; involving the processing of data-words G06F 11/00) [5]
- 1/32 • • Means for saving power [5]

**3/00 Input arrangements for transferring data to be processed into a form capable of being handled by the computer; Output arrangements for transferring data from processing unit to output unit, e.g. interface arrangements** (typewriters B41J; conversion of physical variables F15B 5/00, G01; image acquisition G06T 1/00, G06T 9/00; coding, decoding or code conversion, in general H03M; transmission of digital information H04L) [4]

- 3/01 • Input arrangements or combined input and output arrangements for interaction between user and computer (G06F 3/16 takes precedence) [2006.01]
- 3/02 • • Input arrangements using manually operated switches, e.g. using keyboards or dials (keyboard switches *per se* H01H 13/70; electronic switches characterised by the way in which the control signals are generated H03K 17/94) [3, 2006.01]
- 3/023 • • • Arrangements for converting discrete items of information into a coded form, e.g. arrangements for interpreting keyboard generated codes as alphanumeric codes, operand codes or instruction codes (coding in connection with keyboards or like devices in general H03M 11/00) [3, 2006.01]

- 3/027 • • • • for insertion of the decimal point [3, 2006.01]
- 3/03 • • Arrangements for converting the position or the displacement of a member into a coded form [3, 2006.01]
- Note(s) [2006.01]**
- In this group, the first place priority rule is applied, i.e. at each hierarchical level, classification is made in the first appropriate place.
- 3/033 • • • Pointing devices displaced or positioned by the user, e.g. mice, trackballs, pens or joysticks; Accessories therefor [3, 2006.01]
- 3/037 • • • • using the raster scan of a cathode-ray tube (CRT) for detecting the position of the member, e.g. light pens cooperating with CRT monitors [3, 2006.01]
- 3/038 • • • • Control and interface arrangements therefor, e.g. drivers or device-embedded control circuitry [2006.01]
- 3/039 • • • • Accessories therefor, e.g. mouse pads (furniture aspects A47B 21/00) [2006.01]
- 3/041 • • • Digitisers, e.g. for touch screens or touch pads, characterised by the transducing means [2006.01]
- 3/042 • • • • by opto-electronic means [2006.01]
- 3/043 • • • • using propagating acoustic waves [2006.01]
- 3/044 • • • • by capacitive means [2006.01]
- 3/045 • • • • using resistive elements, e.g. a single continuous surface or two parallel surfaces put in contact [2006.01]
- 3/046 • • • • by electromagnetic means [2006.01]
- 3/047 • • • • using sets of wires, e.g. crossed wires [2006.01]
- 3/048 • • Interaction techniques for graphical user interfaces, e.g. interaction with windows, icons or menus [2006.01]
- 3/05 • Digital input using the sampling of an analogue quantity at regular intervals of time (sample-and-hold arrangements G11C 27/02; sampling *per se* H03K 17/00; analogue/digital conversion, in general H03M 1/00)
- 3/06 • Digital input from, or digital output to, record carriers
- 3/08 • • from or to individual record carriers, e.g. punched card
- 3/09 • Digital output to typewriters [3]
- 3/12 • Digital output to print unit (digital output to typewriter G06F 3/09; arrangements for producing a permanent visual presentation of the output data using printers G06K 15/02)
- 3/13 • Digital output to plotter (arrangements for producing a permanent visual presentation of the output data using plotters G06K 15/22) [3]
- 3/14 • Digital output to display device (arrangements for producing a permanent visual presentation of the output data G06K 15/00; control of display in general G09G)
- 3/147 • • using display panels [3]
- 3/153 • • using cathode-ray tubes [3]
- 3/16 • Sound input; Sound output (conversion of speech into digital information or *vice versa* G10L)
- 3/18 • Digital input from automatic curve follower (automatic curve followers *per se* G06K 11/02) [3]
- 5/00 Methods or arrangements for data conversion without changing the order or content of the data handled** (coding, decoding or code conversion, in general H03M) [4]
- 5/01 • for shifting, e.g. justifying, scaling, normalising [5]
- 5/06 • for changing the speed of data flow, i.e. speed regularising
- 5/08 • • having a sequence of storage locations, the intermediate ones not being accessible for either enqueue or dequeue operations, e.g. using a shift register [2006.01]
- 5/10 • • having a sequence of storage locations each being individually accessible for both enqueue and dequeue operations, e.g. using random access memory [2006.01]
- 5/12 • • • Means for monitoring the fill level; Means for resolving contention, i.e. conflicts between simultaneous enqueue and dequeue operations [2006.01]
- 5/14 • • • • for overflow or underflow handling, e.g. full or empty flags [2006.01]
- 5/16 • • Multiplexed systems, i.e. using two or more similar devices which are alternately accessed for enqueue and dequeue operations, e.g. ping-pong buffers [2006.01]
- 7/00 Methods or arrangements for processing data by operating upon the order or content of the data handled** (logic circuits H03K 19/00)
- 7/02 • Comparing digital values (G06F 7/06, G06F 7/38 take precedence; information retrieval G06F 17/30; comparing pulses H03K 5/22)
- 7/04 • • Identity comparison, i.e. for like or unlike values
- 7/06 • Arrangements for sorting, selecting, merging, or comparing data on individual record carriers (sorting of postal letters B07C; conveying record carriers from one station to another G06K 13/02)
- 7/08 • • Sorting, i.e. grouping record carriers in numerical or other ordered sequence according to the classification of at least some of the information they carry (by merging two or more sets of carriers in ordered sequence G06F 7/16)
- 7/10 • • Selecting, i.e. obtaining data of one kind from those record carriers which are identifiable by data of a second kind from a mass of ordered or randomly-distributed record carriers
- 7/12 • • • with provision for printing-out a list of selected items
- 7/14 • • Merging, i.e. combining at least two sets of record carriers each arranged in the same ordered sequence to produce a single set having the same ordered sequence
- 7/16 • • • Combined merging and sorting
- 7/20 • • Comparing separate sets of record carriers arranged in the same sequence to determine whether at least some of the data in one set is identical with that in the other set or sets
- 7/22 • Arrangements for sorting or merging computer data on continuous record carriers, e.g. tape, drum, disc
- 7/24 • • Sorting, i.e. extracting data from one or more carriers, re-arranging the data in numerical or other ordered sequence, and re-recording the sorted data on the original carrier or on a different carrier or set of carriers (G06F 7/36 takes precedence)
- 7/26 • • • the sorted data being recorded on the original record carrier within the same space in which the data had been recorded prior to their sorting, without using intermediate storage

- 7/32 • • Merging, i.e. combining data contained in ordered sequence on at least two record carriers to produce a single carrier or set of carriers having all the original data in the ordered sequence (G06F 7/36 takes precedence)
- 7/36 • • Combined merging and sorting
- 7/38 • • Methods or arrangements for performing computations using exclusively denominational number representation, e.g. using binary, ternary, decimal representation [3]
- 7/40 • • using contact-making devices, e.g. electromagnetic relay (G06F 7/46 takes precedence)
- 7/42 • • • Adding; Subtracting
- 7/44 • • • Multiplying; Dividing
- 7/46 • • using electromechanical counter-type accumulators
- 7/48 • • using non-contact-making devices, e.g. tube, solid state device; using unspecified devices [3]
- 7/483 • • • Computations with numbers represented by a non-linear combination of denominational numbers, e.g. rational numbers, logarithmic number system, floating-point numbers (conversion to or from floating-point codes H03M 7/24) [2006.01]
- 7/485 • • • • Adding; Subtracting [2006.01]
- 7/487 • • • • Multiplying; Dividing [2006.01]
- 7/49 • • • Computations with a radix, other than binary, 8, 16 or decimal, e.g. ternary, negative or imaginary radices, mixed radix [3]
- 7/491 • • • Computations with decimal numbers [2006.01]
- 7/492 • • • using a binary weighted representation within each denomination [2006.01]
- 7/493 • • • • the representation being the natural binary coded representation, i.e. 8421-code [2006.01]
- 7/494 • • • • • Adding; Subtracting [2006.01]
- 7/495 • • • • • in digit-serial fashion, i.e. having a single digit-handling circuit treating all denominations after each other [2006.01]
- 7/496 • • • • • Multiplying; Dividing [2006.01]
- 7/498 • • • • using counter-type accumulators [2006.01]
- 7/499 • • • Denomination or exception handling, e.g. rounding, overflow [2006.01]
- 7/50 • • • Adding; Subtracting (G06F 7/483-G06F 7/491, G06F 7/544-G06F 7/556 take precedence) [3, 2006.01]
- 7/501 • • • • Half or full adders, i.e. basic adder cells for one denomination (EXCLUSIVE-OR circuits H03K 19/21) [2006.01]
- 7/502 • • • • • Half adders; Full adders consisting of two cascaded half adders [2006.01]
- 7/503 • • • • • using carry switching, i.e. the incoming carry being connected directly, or only via an inverter, to the carry output under control of a carry propagate signal [2006.01]
- 7/504 • • • • in bit-serial fashion, i.e. having a single digit-handling circuit treating all denominations after each other [2006.01]
- 7/505 • • • • in bit-parallel fashion, i.e. having a different digit-handling circuit for each denomination (half or full adders G06F 7/501) [2006.01]
- 7/506 • • • • • with simultaneous carry generation for, or propagation over, two or more stages [2006.01]
- 7/507 • • • • • using selection between two conditionally calculated carry or sum values [2006.01]
- 7/508 • • • • • using carry look-ahead circuits [2006.01]
- 7/509 • • • • • for multiple operands, e.g. digital integrators [2006.01]
- 7/52 • • • Multiplying; Dividing (G06F 7/483-G06F 7/491, G06F 7/544-G06F 7/556 take precedence) [3, 2006.01]
- 7/523 • • • • Multiplying only [2006.01]
- 7/525 • • • • • in serial-serial fashion, i.e. both operands being entered serially (G06F 7/533 takes precedence) [2006.01]
- 7/527 • • • • • in serial-parallel fashion, i.e. one operand being entered serially and the other in parallel (G06F 7/533 takes precedence) [2006.01]
- 7/53 • • • • • in parallel-parallel fashion, i.e. both operands being entered in parallel (G06F 7/533 takes precedence) [2006.01]
- 7/533 • • • • • Reduction of the number of iteration steps or stages, e.g. using the Booth algorithm, log-sum, odd-even [2006.01]
- 7/535 • • • • • Dividing only [2006.01]
- 7/537 • • • • • Reduction of the number of iteration steps or stages, e.g. using the Sweeny-Robertson-Tocher (SRT) algorithm [2006.01]
- 7/544 • • • • for evaluating functions by calculation (with a look-up table G06F 1/02) [3]
- 7/548 • • • • Trigonometric functions; Co-ordinate transformations [3]
- 7/552 • • • • Powers or roots [3]
- 7/556 • • • • Logarithmic or exponential functions [3]
- 7/57 • • • Arithmetic logic units (ALU), i.e. arrangements or devices for performing two or more of the operations covered by groups G06F 7/483-G06F 7/556 or for performing logical operations (instruction execution G06F 9/30) [2006.01]
- 7/575 • • • • Basic arithmetic logic units, i.e. devices selectable to perform either addition, subtraction or one of several logical operations, using, at least partially, the same circuitry [2006.01]
- 7/58 • Random or pseudo-random number generators [3]
- 7/60 • Methods or arrangements for performing computations using a digital non-denominational number representation, i.e. number representation without radix; Computing devices using combinations of denominational and non-denominational quantity representations [3]
- 7/62 • • Performing operations exclusively by counting total number of pulses [3]
- 7/64 • • Digital differential analysers, i.e. computing devices for differentiation, integration or solving differential or integral equations, using pulses representing increments; Other incremental computing devices for solving difference equations (G06F 7/70 takes precedence; differential analysers using hybrid computing techniques G06J 1/02) [3]
- 7/66 • • • wherein pulses represent unitary increments only [3]
- 7/68 • • using pulse rate multipliers or dividers (G06F 7/70 takes precedence) [3]

- 7/70 • • using stochastic pulse trains, i.e. randomly occurring pulses the average pulse rates of which represent numbers [3]
- 7/72 • • using residue arithmetic [3]
- 7/74 • Selecting or encoding within a word the position of one or more bits having a specified value, e.g. most or least significant one or zero detection, priority encoders [2006.01]
- 7/76 • Arrangements for rearranging, permuting or selecting data according to predetermined rules, independently of the content of the data (according to the content of the data G06F 7/06, G06F 7/22; parallel/series conversion or *vice versa* H03M 9/00) [2006.01]
- 7/78 • • for changing the order of data flow, e.g. matrix transposition, LIFO buffers; Overflow or underflow handling therefor [2006.01]
- 9/00 Arrangements for programme control, e.g. control unit** (programme control for peripheral devices G06F 13/10) [4]
  - 9/02 • using wired connections, e.g. plugboard
  - 9/04 • using record carriers containing only programme instructions (G06F 9/06 takes precedence)
  - 9/06 • using stored programme, i.e. using internal store of processing equipment to receive and retain programme
  - 9/22 • • Micro-control or micro-programme arrangements [3]
  - 9/24 • • • Loading of the micro-programme [3]
  - 9/26 • • • Address formation of the next micro-instruction (G06F 9/28 takes precedence) [3]
  - 9/28 • • • Enhancement of operational speed, e.g. by using several micro-control devices operating in parallel [3]
  - 9/30 • • Arrangements for executing machine- instructions, e.g. instruction decode (for executing micro-instructions G06F 9/22; for executing subprogrammes G06F 9/40) [3]
  - 9/302 • • • Controlling the executing of arithmetic operations [5]
  - 9/305 • • • Controlling the executing of logical operations [5]
  - 9/308 • • • Controlling single bit operations (G06F 9/305 takes precedence) [5]
  - 9/312 • • • Controlling loading, storing or clearing operations [5]
  - 9/315 • • • Controlling moving, shifting or rotation operations [5]
  - 9/318 • • • with operation extension or modification [5]
  - 9/32 • • • Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]
  - 9/34 • • • Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]
  - 9/345 • • • • of multiple operands or results [5]
  - 9/35 • • • • Indirect addressing [5]
  - 9/355 • • • • Indexed addressing [5]
  - 9/38 • • • Concurrent instruction execution, e.g. pipeline, look ahead [3]
  - 9/40 • • Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]
  - 9/42 • • • Formation of subprogramme-jump address or of return address [3]
  - 9/44 • • Arrangements for executing specific programmes [3]
- 9/445 • • • Programme loading or initiating [5]
- 9/45 • • • Compilation or interpretation of high level programme languages [5]
- 9/455 • • • Emulation; Software simulation [5]
- 9/46 • • Multiprogramming arrangements [3]
- 9/48 • • • Programme initiating; Programme switching, e.g. by interrupt [7]
- 9/50 • • • Allocation of resources, e.g. of the central processing unit (CPU) [7]
- 9/52 • • • Programme synchronisation; Mutual exclusion, e.g. by means of semaphores [7]
- 9/54 • • • Interprogramme communication [7]
- 11/00 Error detection; Error correction; Monitoring** (methods or arrangements for verifying the correctness of marking on a record carrier G06K 5/00; in information storage based on relative movement between record carrier and transducer G11B, e.g. G11B 20/18; in static stores G11C 29/00; coding, decoding or code conversion, for error detection or error correction, in general H03M 13/00) [4]
  - 11/07 • Responding to the occurrence of a fault, e.g. fault tolerance [7]
  - 11/08 • • Error detection or correction by redundancy in data representation, e.g. by using checking codes
  - 11/10 • • • Adding special bits or symbols to the coded information, e.g. parity check, casting out nines or elevens
  - 11/14 • • Error detection or correction of the data by redundancy in operation, e.g. by using different operation sequences leading to the same result (G06F 11/16 takes precedence) [3]
  - 11/16 • • Error detection or correction of the data by redundancy in hardware [3]
  - 11/18 • • • using passive fault-masking of the redundant circuits, e.g. by quadding or by majority decision circuits [3]
  - 11/20 • • • using active fault-masking, e.g. by switching out faulty elements or by switching in spare elements [3]
  - 11/22 • Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing (testing of digital circuits, e.g. of separate computer components, G01R 31/317) [3]
  - 11/24 • • Marginal testing [3]
  - 11/25 • • Testing of logic operation, e.g. by logic analysers [6]
  - 11/26 • • Functional testing [3]
  - 11/263 • • • Generation of test inputs, e.g. test vectors, patterns or sequences [6]
  - 11/267 • • • Reconfiguring circuits for testing, e.g. LSSD, partitioning [6]
  - 11/27 • • • Built-in tests [6]
  - 11/273 • • • Tester hardware, i.e. output processing circuits [6]
  - 11/277 • • • • with comparison between actual response and known fault-free response [6]
  - 11/28 • by checking the correct order of processing (G06F 11/07, G06F 11/22 take precedence; monitoring patterns of pulse trains H03K 5/19) [3]
  - 11/30 • Monitoring [3]
  - 11/32 • • with visual indication of the functioning of the machine [3]
  - 11/34 • • Recording or statistical evaluation of computer activity, e.g. of down time, of input/output operation [3]

- 11/36 • Preventing errors by testing or debugging of software [7]
- 12/00 Accessing, addressing or allocating within memory systems or architectures** (information storage in general G11) [4, 5]
- 12/02 • Addressing or allocation; Relocation (programme address sequencing G06F 9/00; arrangements for selecting an address in a digital store G11C 8/00) [4]
- 12/04 • • Addressing variable-length words or parts of words [4]
- 12/06 • • Addressing a physical block of locations, e.g. base addressing, module addressing, address space extension, memory dedication (G06F 12/08 takes precedence) [4]
- 12/08 • • in hierarchically structured memory systems, e.g. virtual memory systems [4]
- 12/10 • • • Address translation [4]
- 12/12 • • • Replacement control [4]
- 12/14 • Protection against unauthorised use of memory [4]
- 12/16 • Protection against loss of memory contents [4]
- 13/00 Interconnection of, or transfer of information or other signals between, memories, input/output devices or central processing units** (interface circuits for specific input/output devices G06F 3/00; multi-processor systems G06F 15/16; transmission of digital information in general H04L; selecting H04Q) [4]
- 13/10 • Programme control for peripheral devices (G06F 13/14-G06F 13/42 take precedence) [4]
- 13/12 • • using hardware independent of the central processor, e.g. channel or peripheral processor [4]
- 13/14 • Handling requests for interconnection or transfer [4]
- 13/16 • • for access to memory bus (G06F 13/28 takes precedence) [4]
- 13/18 • • • with priority control [4]
- 13/20 • • for access to input/output bus [4]
- 13/22 • • • using successive scanning, e.g. polling (G06F 13/24 takes precedence) [4]
- 13/24 • • • using interrupt (G06F 13/32 takes precedence) [4]
- 13/26 • • • with priority control [4]
- 13/28 • • • using burst mode transfer, e.g. direct memory access, cycle steal (G06F 13/32 takes precedence) [4]
- 13/30 • • • • with priority control [4]
- 13/32 • • • using combination of interrupt and burst mode transfer [4]
- 13/34 • • • • with priority control [4]
- 13/36 • • for access to common bus or bus system [4]
- 13/362 • • • with centralised access control [5]
- 13/364 • • • • using independent requests or grants, e.g. using separated request and grant lines [5]
- 13/366 • • • • using a centralised polling arbiter [5]
- 13/368 • • • • with decentralised access control [5]
- 13/37 • • • • using a physical-position-dependent priority, e.g. daisy chain, round robin or token passing [5]
- 13/372 • • • • using a time-dependent priority, e.g. individually loaded time counters or time slot [5]
- 13/374 • • • • using a self-select method with individual priority code comparator [5]
- 13/376 • • • • using a contention resolving method, e.g. collision detection, collision avoidance [5]
- 13/378 • • • • using a parallel poll method [5]
- 13/38 • Information transfer, e.g. on bus (G06F 13/14 takes precedence) [4]
- 13/40 • • Bus structure [4]
- 13/42 • • Bus transfer protocol, e.g. handshake; Synchronisation (synchronisation in transmission of digital information in general H04L 7/00) [4]
- 15/00 Digital computers in general** (details G06F 1/00-G06F 13/00); **Data processing equipment in general** (neural networks for image data processing G06T)
- 15/02 • manually operated with input through keyboard and computation using a built-in programme, e.g. pocket calculators
- 15/04 • programmed simultaneously with the introduction of data to be processed, e.g. on the same record carrier
- 15/08 • using a plugboard for programming [5]
- 15/10 • • Tabulators [5]
- 15/12 • • • having provision for both printed and punched output [5]
- 15/14 • • Calculating-punches [5]
- 15/16 • Combinations of two or more digital computers each having at least an arithmetic unit, a programme unit and a register, e.g. for a simultaneous processing of several programmes (interface circuits for specific input/output devices G06F 3/00; multi-programming arrangements G06F 9/46; transmission of digital information in general H04L, e.g. in computer networks H04L 12/00; selecting H04Q)
- 15/163 • • Interprocessor communication [6]
- 15/167 • • • using a common memory, e.g. mailbox (memory protection G06F 12/14; memory access priority G06F 13/18) [6]
- 15/17 • • • using an input/output type connection, e.g. channel, I/O port [6]
- 15/173 • • • using an interconnection network, e.g. matrix, shuffle, pyramid, star, snowflake (interface switching circuits G06F 13/40) [6]
- 15/177 • • Initialisation or configuration control (configuration control for monitoring, testing or in case of failure G06F 11/00) [6]
- 15/18 • in which a programme is changed according to experience gained by the computer itself during a complete run; Learning machines (adaptive control systems G05B 13/00)
- 15/76 • Architectures of general purpose stored programme computers (with programme plugboard G06F 15/08; multicomputers G06F 15/16; general purpose image data processing G06T 1/00) [5, 6]
- 15/78 • • comprising a single central processing unit [5]
- 15/80 • • comprising an array of processing units with common control, e.g. single instruction multiple data processors (G06F 15/82 takes precedence) [5]
- 15/82 • • data or demand driven [5]
- 17/00 Digital computing or data processing equipment or methods, specially adapted for specific functions** [6]
- 17/10 • Complex mathematical operations [6]
- 17/11 • • for solving equations [6]
- 17/12 • • • Simultaneous equations [6]
- 17/13 • • • Differential equations (using digital differential analysers G06F 7/64) [6]
- 17/14 • • Fourier, Walsh or analogous domain transformations [6]
- 17/15 • • Correlation function computation [6]
- 17/16 • • Matrix or vector computation [6]

- 17/17 • • Function evaluation by approximation methods, e.g. inter- or extrapolation, smoothing, least mean square method (interpolation for numerical control G05B 19/18) [6]
- 17/18 • • for evaluating statistical data [6]
- 17/20 • Handling natural language data (speech analysis or synthesis G10L) [6]
- 17/21 • • Text processing (G06F 17/27, G06F 17/28 take precedence; systems for composing machines B41B 27/00) [6]
- 17/22 • • • Manipulating or registering by use of codes, e.g. in sequence of text characters [6]
- 17/24 • • • Editing, e.g. insert/delete [6]
- 17/25 • • • Automatic justification [6]
- 17/26 • • • Automatic hyphenation [6]
- 17/27 • • Automatic analysis, e.g. parsing, orthograph correction [6]
- 17/28 • • Processing or translating of natural language (G06F 17/27 takes precedence) [6]
- 17/30 • Information retrieval; Database structures therefor [6]
- 17/40 • Data acquisition and logging (for input to computer G06F 3/00) [6]
- 17/50 • Computer-aided design (for the design of test circuits for static stores G11C 29/54) [6, 2006.01]
- 19/00** **Digital computing or data processing equipment or methods, specially adapted for specific applications** (G06F 17/00 takes precedence; data processing systems or methods specially adapted for administrative, commercial, financial, managerial, supervisory or forecasting purposes G06Q) [6, 2006.01, 2011.01]
- Note(s)**
- This group covers:
- special constructions of computers to permit or facilitate use in specific applications;
  - non-structural adaptations of computers to a specific application, e.g. computing methods.
- 19/10 • *Bioinformatics, i.e. methods or systems for genetic or protein-related data processing in computational molecular biology (in silico methods of screening virtual chemical libraries C40B 30/02; in silico or mathematical methods of creating virtual chemical libraries C40B 50/02) [2011.01]*
- Note(s) [2011.01]**
1. This group also covers bioinformatics methods or systems where digital data processing is inherent or implicit, but not explicitly mentioned.
  2. In this group, the following term is used with the meaning indicated:
    - "systems" includes apparatus.
  3. In this group, at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place.
- 19/12 • • for modelling or simulation in systems biology, e.g. probabilistic or dynamic models, gene-regulatory networks, protein interaction networks or metabolic networks [2011.01]
- 19/14 • • for phylogeny or evolution, e.g. evolutionarily conserved regions determination or phylogenetic tree construction [2011.01]
- 19/16 • • for molecular structure, e.g. structure alignment, structural or functional relations, protein folding, domain topologies, drug targeting using structure data, involving two-dimensional or three-dimensional structures [2011.01]
- 19/18 • • for functional genomics or proteomics, e.g. genotype-phenotype associations, linkage disequilibrium, population genetics, binding site identification, mutagenesis, genotyping or genome annotation, protein-protein interactions or protein-nucleic acid interactions [2011.01]
- 19/20 • • for hybridisation or gene expression, e.g. microarrays, sequencing by hybridisation, normalisation, profiling, noise correction models, expression ratio estimation, probe design or probe optimisation [2011.01]
- 19/22 • • for sequence comparison involving nucleotides or amino acids, e.g. homology search, motif or SNP [Single-Nucleotide Polymorphism] discovery or sequence alignment [2011.01]
- 19/24 • • for machine learning, data mining or biostatistics, e.g. pattern finding, knowledge discovery, rule extraction, correlation, clustering or classification [2011.01]
- 19/26 • • for data visualisation, e.g. graphics generation, display of maps or networks or other visual representations [2011.01]
- 19/28 • • for programming tools or database systems, e.g. ontologies, heterogeneous data integration, data warehousing or computing architectures [2011.01]
- 21/00** **Security arrangements for protecting computers or computer systems against unauthorised activity** (multiprogramming G06F 9/46; protection against unauthorised use of memory G06F 12/14; dispensing apparatus actuated by coded identity card or credit card G07F 7/08; equipment anti-theft monitoring by a central station G08B 26/00; secret or secure communication H04L 9/00; data switching networks H04L 12/00) [2006.01]
- 21/02 • by protecting specific internal components of computers [2006.01]
- 21/04 • by protecting specific peripheral devices, e.g. keyboards or displays [2006.01]
- 21/06 • by sensing unauthorised manipulation of, or intrusion into, an enclosure e.g. a housing or a room [2006.01]
- 21/20 • by restricting access to nodes in a computer system or computer network [2006.01]
- 21/22 • by restricting access to, or manipulation of, programmes or processes [2006.01]
- 21/24 • by protecting data directly, e.g. by labelling [2006.01]